

(Use as many sheets as necessary)

SHEET 1 of 2

Complete if Known

Application Number	To Be Assigned	10/712,971
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Filing Date ~~Herewith~~ 11/13/2003

First Named Inventor	T. Nakatani et al.
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Art Unit	2685
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Examiner Name R. Vishwa

Attorney Docket No. MTS-3482US

[illegible]

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date (MM-DD-YYYY)	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (If known)				
<i>a</i>		JP 11-501466 (corresponds to US 5,681,761)	2/2/1999	Kim	Entire Document	<input type="checkbox"/>
<i>a</i>		JP 8-316420 (w/abstract & partial translation)	11/29/1996	Hitachi Ltd	Entire Document	<input type="checkbox"/>
<i>a</i>		JP 8-17847 (corresponds to US 5,485,029)	1/19/1996	IBM	Entire Document	<input type="checkbox"/>
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Examiner
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3/18/06

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

¹Applicant's unique citation designation number (optional).

²See Kind Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04.

³Enter Office that issued the document, by the two-letter code (WIPO Standard St.3).

⁴For Japanese patent documents, the Indication of the year of the reign of the Emperor must precede the serial number of the patent document.

⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible.

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Substitute for Form 1449B/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	To Be Assigned	10/712 971
Filing Date	Herewith	11/13/2003
First Named Inventor	T/ Matatamo et al.	
Art Unit		
Examiner Name	D. VUONG	
Attorney Docket No.	MTS-3482US	

SHEET 2 of 2

NON-PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
<i>AV</i>		M. Tiebout, "Low-Power Low-Phase-Noise Differentially Tuned Quadrature VCO Design in Standard CMOS", IEEE Journal of Solid-State Circuits, vol. 36, no. 7, pp. 1018-1024, July 2001.	<input type="checkbox"/>
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Examiner
Signature*Christian B. Vuong*Date
Considered

3/18/06

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